

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

**U.S. UTILITY Patent Application**

<p>O.I.P.E. <b>STTH</b></p> <p>SCANNED <b>LM1</b> Q.A. <b>[initials]</b></p>	PATENT DATE
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APPLICATION NO. 09/683027	CONT/PRIOR	CLASS <del>301</del> 257	SUBCLASS 724	ART UNIT <del>2835</del> 2827	EXAMINER THAI
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TITLE	APPLICANTS
1. <u>Director</u>	1. <u>Mr. J. H. Smith</u>
2. <u>Assistant Director</u>	2. <u>Mr. J. H. Smith</u>
3. <u>Chief Clerk</u>	3. <u>Mr. J. H. Smith</u>
4. <u>Chief of Police</u>	4. <u>Mr. J. H. Smith</u>
5. <u>Chief of Fire</u>	5. <u>Mr. J. H. Smith</u>
6. <u>Chief of Health</u>	6. <u>Mr. J. H. Smith</u>
7. <u>Chief of Education</u>	7. <u>Mr. J. H. Smith</u>
8. <u>Chief of Social Work</u>	8. <u>Mr. J. H. Smith</u>
9. <u>Chief of Public Works</u>	9. <u>Mr. J. H. Smith</u>
10. <u>Chief of Finance</u>	10. <u>Mr. J. H. Smith</u>
11. <u>Chief of Law</u>	11. <u>Mr. J. H. Smith</u>
12. <u>Chief of Planning</u>	12. <u>Mr. J. H. Smith</u>
13. <u>Chief of Administration</u>	13. <u>Mr. J. H. Smith</u>
14. <u>Chief of Information</u>	14. <u>Mr. J. H. Smith</u>
15. <u>Chief of Research</u>	15. <u>Mr. J. H. Smith</u>
16. <u>Chief of Development</u>	16. <u>Mr. J. H. Smith</u>
17. <u>Chief of Evaluation</u>	17. <u>Mr. J. H. Smith</u>
18. <u>Chief of Policy</u>	18. <u>Mr. J. H. Smith</u>
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23. <u>Chief of Innovation</u>	23. <u>Mr. J. H. Smith</u>
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57. <u>Chief of Wisdom</u>	57. <u>Mr. J. H. Smith</u>
58. <u>Chief of Experience</u>	58. <u>Mr. J. H. Smith</u>
59. <u>Chief of Expertise</u>	59. <u>Mr. J. H. Smith</u>
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62. <u>Chief of Capability</u>	62. <u>Mr. J. H. Smith</u>
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66. <u>Chief of Optimism</u>	66. <u>Mr. J. H. Smith</u>
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87. <u>Chief of Strategy</u>	87. <u>Mr. J. H. Smith</u>
88. <u>Chief of Operations</u>	88. <u>Mr. J. H. Smith</u>
89. <u>Chief of Management</u>	89. <u>Mr. J. H. Smith</u>
90. <u>Chief of Leadership</u>	90. <u>Mr. J. H. Smith</u>
9	

John Malinowski  
Edmund Sprogis  
Steven Voldman

Dual chip stack method for electro-static discharge protection of integrated circuits

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

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